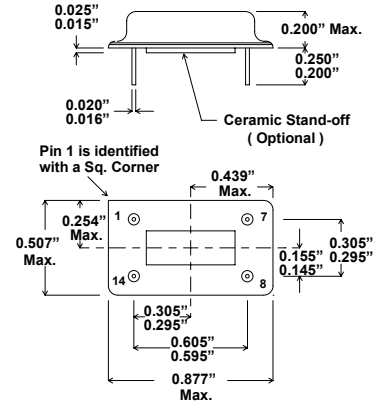


## T200 SERIES (CMOS)

### STANDARD SPECIFICATIONS

FREQUENCY RANGE	1.5 Hz to 12 MHz
FREQUENCY ACCURACY @ +25 °C	± 0.0015% ( ± 15 PPM )
FREQUENCY STABILITY Vs. TEMPERATURE	See Options Below
OPERATING TEMPERATURE RANGE	See Options Below
INPUT VOLTAGE ( See note below )	+ 5 VDC to + 15 VDC ±
INPUT CURRENT	5 mA Max. @ + 5 V 25 mA Max. @ + 15 V
OUTPUT	CMOS
LOAD	200 KΩ in parallel with 5
SYMMETRY	60/40% @ 50% Output
RISE & FALL TIMES ( 10% to 90% Output Level )	150 nS Max. @ + 5 VDC 50 nS Max. @ + 15 VDC
START-UP TIME	
< 10 MHz	20 mS Max.
≥ 10 MHz	15 mS Max.
FREQUENCY STABILITY Vs. VOLTAGE	± 0.0005% ( ± 5 PPM ) Max. ( for 10% change in Voltage )
AGING @ +25 °C	± 0.0005% ( ± 5 PPM ) / year Max.
PACKAGE, SEAL & LEAD FINISH	Conforms with the Requirements of MIL-PRF-55310



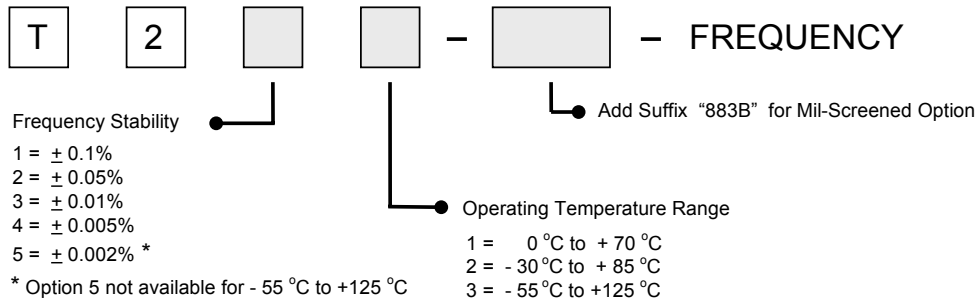
**Pin Connections**

14	B+
7	GND
8	OUTPUT
1	N/C
All Others	MISSING

**Note:** Input Voltage must be specified for 200 Series CMOS parts, minimum input voltage required depends upon output frequency and operating temperature range. Consult factory for your specific application.

**Contact Xsis Engineering** for special requirements such as, **Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.**

**ORDERING INFORMATION ( Select from options below ) :**



**EXAMPLE:** T243 - 883B - 4.000 MHz = 4 Pin Package, CMOS, ± 0.005% over -55 °C to +125 °C, Mil-Screened, and 20.000 MHz