

XLOO SERIES (HC/ACMOS/TTL), 3.3 VDC (Enable/Disable Option on Pin 1)

FREQUENCY RANGE	100 KHz to 90 MHz
FREQUENCY ACCURACY @ +25 °C	± 0.0015% (± 15 PPM)
FREQUENCY STABILITY Vs. TEMPERATURE	See Options Below
OPERATING TEMPERATURE RANGE	See Options Below
INPUT VOLTAGE	+ 3.3 VDC ± 10%

INPUT CURRENT @ +3.30 VDC	
100.0 KHz to 5.0 MHz	3 mA Max.
5.1 MHz to 12.0 MHz	6 mA Max.
12.1 MHz to 20.0 MHz	10 mA Max.
20.1 MHz to 35.0 MHz	15 mA Max.
35.1 MHz to 50.0 MHz	20 mA Max.
50.1 MHz to 100.0 MHz	30 mA Max.

OUTPUT LOAD		HC/ACMOS/TTL
	HC/ACMOS	10 KΩ in parallel with 15 pf
	TTL	10 TTL
SYMMETRY		≤ 10.0 MHz 45/55%
		> 10.0 MHz 40/60%

RISE & FALL TIMES (10% to 90% Level)	
≤ 25 MHz	10 nS Max.
> 25 MHz	3 nS Max.

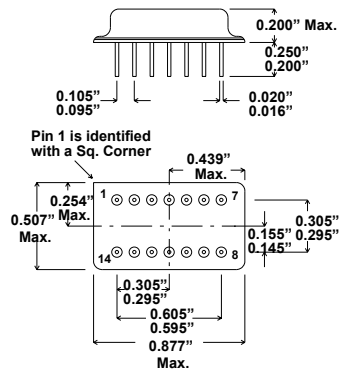
START-UP TIME	10 mS Max.
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JITTER (RMS) @ 25Cr	15 pS Typ <40 mHz; 8 ps Typ.>40 MHz
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FREQUENCY STABILITY Vs. VOLTAGE	± 0.0004% (± 4 PPM) Max. for 10% change in Voltage
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AGING @ +25 °C	± 0.0005% (± 5 PPM) / year Max.
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PACKAGE, SEAL & LEAD FINISH	Conforms with the Requirements of MIL-PRF-55310
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Pin Connections

14	B+
7	GND
8	OUTPUT
1	ENABLE/DISABLE (Option)
All Others	N/C

Contact Xsis Engineering for special requirements such as, **Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.**

ORDERING INFORMATION (Select from options below) :

X	L	□	□	□	-	□	-	FREQUENCY
Frequency Stability		Operating Temperature Range		Add Suffix "E" for Enable/Disable Option **		Add Suffix "883B" for Mil-Screened Option		
1 = ± 0.1%		1 = 0 °C to +70 °C		** Enable/Disable Enable/Disable Input is on Pin 1. A "high" level at the input enables the output. A low level at the input disables the output in to a high impedance state. Enable/Disable input has an internal pull-up.				
2 = ± 0.05%		2 = -30 °C to +85 °C						
3 = ± 0.01%		3 = -55 °C to +125 °C						
4 = ± 0.005%								
5 = ± 0.002% *								

* Option 5 not available for -55 °C to +125 °C

EXAMPLE: XL43 - 883B - 24.000 MHz = 14 Pin Package, HCMOS, ± 0.005% over -55 °C to +125 °C, Mil-Screened , and 24.000 MHz